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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(211.001-D3-US)

In re Application of: FAZAN ET AL.)	
Serial No: 10/724,648)	Group Art Unit: 2814
Filing Date: DECEMBER 1, 2003)	Examiner: LOUIE, WAI SING
Title: SEMICONDUCTOR DEVICE)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY OF FEBRUARY 3, 2005

This Reply seeks to place this application in condition for allowance. The rejections set forth in the Office Action of January 21, 2005 (hereinafter the "Office Action") have been addressed herein. In this regard, a Terminal Disclaimer is submitted herewith, executed by the attorney of record, to address the Examiner's concerns regarding obviousness-type double patenting in view of certain claims of co-pending Application No. 10/724,377. This Terminal Disclaimer is submitted to obviate all of the double patenting rejections. It is believed that the Terminal Disclaimer complies fully with the relevant parts of 37 CFR 1.321.

Notably, no inference or conclusion should be drawn that Applicants agree, in any way, with the rejection. Because the obviousness-type double patenting rejection is overcome, without more, via submission of the above-referenced Terminal Disclaimer, Applicants do not provide substantive remarks regarding the merits of the rejection.

CONCLUSION

Applicants respectfully request entry of the attached Terminal Disclaimer issuance of a Notice of Allowance. It is noted that should a telephone interview expedite the prosecution of this application in any way, the Examiner is invited to contact the undersigned at the telephone number listed below.

Date: February 3, 2005

Respectfully submitted,



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